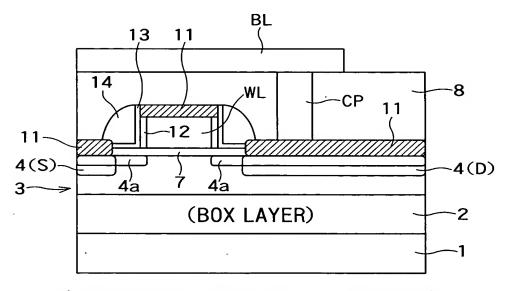


(FBC CELL PORTION)
FIG. 1



(FBC PERIPHERY CIRCUIT PORTION)
FIG. 2

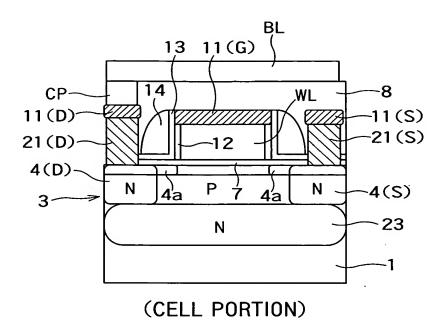
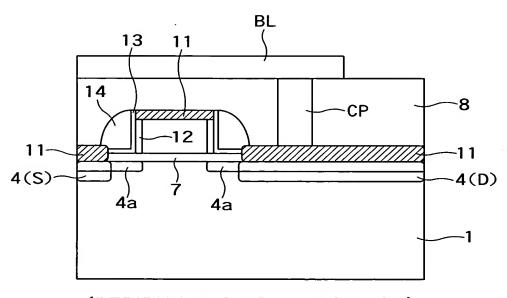
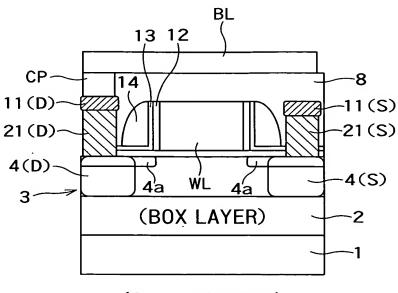


FIG. 3

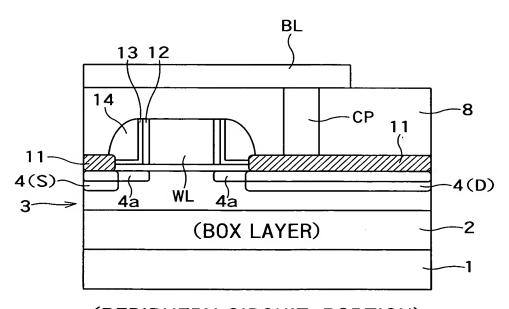


(PERIPHERY CIRCUIT PORTION)
FIG. 4



(CELL PORTION)

FIG. 5



(PERIPHERY CIRCUIT PORTION)

FIG. 6

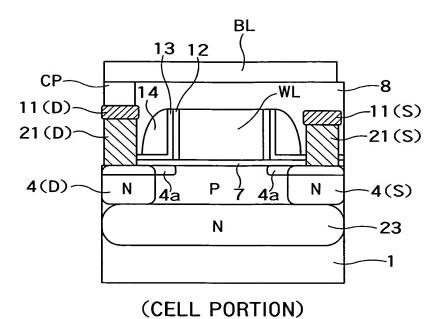
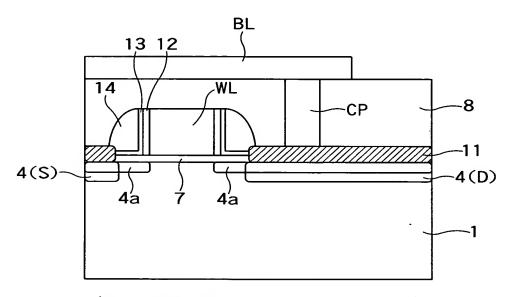


FIG. 7



(PERIPHERY CIRCUIT PORTION)

FIG. 8

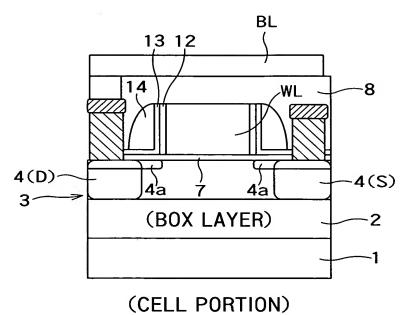
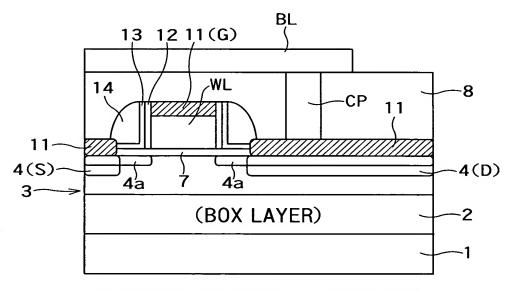
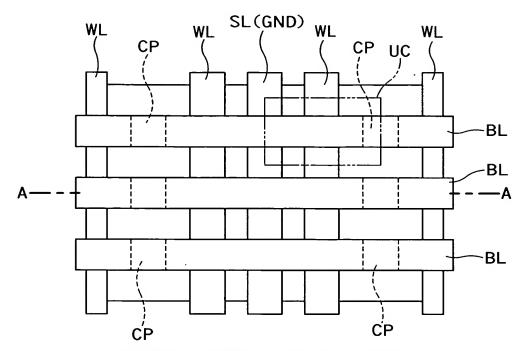


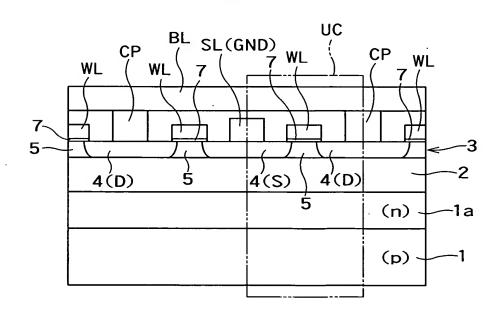
FIG. 9



(PERIPHERY CIRCUIT PORTION)
FIG. 10



(PLAN VIEW OF FBC MEMORY)
FIG. 11



(SECTIONAL VIEW OF FBC MEMORY)
FIG. 12

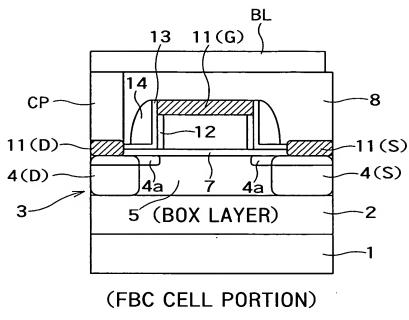
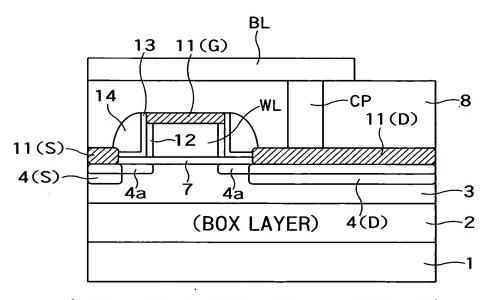


FIG. 13



(FBC PERIPHERY CIRCUIT PORTION)
FIG. 14